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(71) Applicant (for all designated States except US): KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): GAJADHARS-ING, Radjindrepersad [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). ROEDLE, Thomas, C. [DE/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). HAMMES, Petra, C., A. [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). THEEUWEN, Stephan, J., C., H. [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

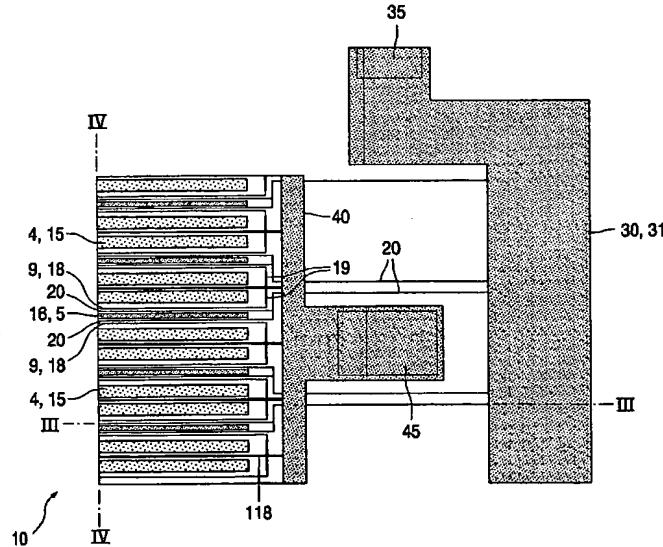
(74) Agent: ELEVeld, Koop, J.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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## (54) Title: SEMICONDUCTOR DEVICE AND METHOD OF OPERATING SUCH A DEVICE



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(57) Abstract: The invention relates to in particular a lateral DMOST with a drain extension (8). In the known transistor a further metal strip (20) is positioned between the gate electrode contact strip and the drain contact (16) which is electrically connected with the source region contact (15). In the device proposed here, the connection between the further metal strip (20) and the source contact (15,12) comprises a capacitor (30) and the further metal strip (20) is provided with a further contact region (35) for delivering a voltage to the further metal strip (20). In this way an improved linearity is possible and the usefulness of the device is improved in particular at high power and at high frequencies. Preferably the capacitor (30) is integrated with the transistor in a single semiconductor body (1). The invention further comprises a method of operating a device (10) according to the invention.

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